		H.T.No.	
Code No: EC1545 GEC-			C-R14
		IV B. Tech I Semester Regular Examinations, November 2017	
		ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	
<b></b>		(Electronics and Communication Engineering)	1 (0
		3 Hours Max. Max	rks: 60
No	te:	All Questions from <b>PART-A</b> are to be answered at one place.  Answer any <b>FOUR</b> questions from <b>PART-B</b> . All Questions carry equal Marks.	
		PART-A	
1	Lic		= 12M
1. 2.		st any four sources of error except the error associated with Hardware.  Calculate the resolution of a 4-1/2 digit voltmeter.	
۷.	a) b)		
3.	a)		
٥.	,	What is the significance of random noise generator?	
4.		What is the purpose of Schering bridge?	
		What is the principle of harmonic distortion analyzer?	
5.	Wl	hat is the need for attenuator in vertical input system of a CRO?	
6.	Wl	hat are the limitations of Piezo electric transducers?	
		PART-B	
		$4 \times 12$	=48M
1.	a)	Draw a shunt type Ohmmeter and explain the calibration procedure in detail.	(8M)
	b)	The expected value of the voltmeter across a resistor is 100V. However, the	
		measurement gives a value of 99V. Calculate	(4M)
		i) Absolute error ii) % error iii) Relative accuracy iv) % accuracy.	
2.	a)	Draw block diagram of Integrating type DVM and explain its operation.	(6M)
	b)	An integrator contains a $100 K\Omega$ and $1 \mu F$ capacitor. If the voltage applied to the integrator	grator
		input is 1V, what voltage will be present at the output of the integrator after 1 sec?	(6M)
3.	a)	What is random noise generator? Explain with neat block diagram.	(6M)
	b)	Describe with neat diagram, the operation of AF sine and square wave generator.	(6M)
4.	a)	Explain the principle and working of Heterodyne Harmonic Distortion Analyzer.	(8M)

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5. a) Draw and explain the block diagram of digital storage oscilloscope.

b) Write a short note on distributed parameter delay line.

6. a) Describe the construction and working of an LVDT.

b) Explain the bonded and unbonded strain gauges.

b) What is harmonic distortion?

(4M)

(7M)

(5M)

(6M)

(6M)